

**Department Of Electrical And Electronics Engineering (Sec Sasaram)**

**Time Table For B.Tech (EEE) 6<sup>th</sup> Semester. Effective From 06/05/2022**

WEEKDAY	10:00-11:00 AM	11:00AM-12:00PM	12:00PM-01:00PM		2:00PM-5:00PM
MONDAY	DSP(VR)	VLSI(VKS)	M&I (RK)		DSP LAB
TUESDAY	PE1(RK)	M&I (RK)	PE2(SS)		VLSI LAB
WEDNESDAY	VLSI(VKS)	DSP(VR)	M&I (RK)		M&I LAB
THURSDAY	PE1(RK)	PE2(SS)	VLSI(VKS)		EDL
FRIDAY	PE2(SS)	M&I (RK)	PEL 1(DMK)		M&I LAB
SATURDAY	PEL1 (DMK)	PE1(RK)	DSP(VR)		DSP ALB
<p><b>PEL 1 Professional Elective Laboratory-1 – Dr Manish kumar(DMK)</b></p> <p><b>DIGITAL SIGNAL PROCESSING (DSP) Mr. VIKASH RAJAK (VR)(100606)</b></p> <p><b>INTRODUCTION TO VLSI DESIGN (VLSI)Mr.VIKASH KUMAR SINGH (VKS) (100607)</b></p> <p><b>MEASUREMENTS AND INSTRUMENT (M&amp;I) Mr. Raushan kumar (RK)(110601)</b></p> <p><b>PE 1ELECTRICAL ENERGY CONSERVATION AND AUDITING (Mr. RAMAN KUMAR) ((PROGRAM ELECTIVE 1) (110608)</b></p> <p><b>PE 2ELECTRICAL MACHINE DESIGN (PROGRAM ELECTIVE 2)(MR. SAILESH SAURABH)(SS)(110609)</b></p> <p><b>ELECTRONICS DESIGN LAB (EDL) (Mr. AMIT KUMAR)(110609P)</b></p>					